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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: UMESH GUPTA ET AL.

Filed: OCTOBER 3, 2001

For: RESOURCE MANAGEMENT ARCHITECTURE FOR USE IN INFORMATION STORAGE ENVIRONMENTS

Serial No.: 09/970,457

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Group Art Unit: 3753

MAR 28 2002

Examiner: UNKNOWN

Technology Center 2100

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Atty Docket No.: SURG:160

FEB 25 2002
TECHNOLOGY CENTER R3700

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in a first class, postage prepaid envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on the date below:

2-8-02

Marty Bower

Date

Name

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document(s) listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R. §§ 1.97(g),(h), this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

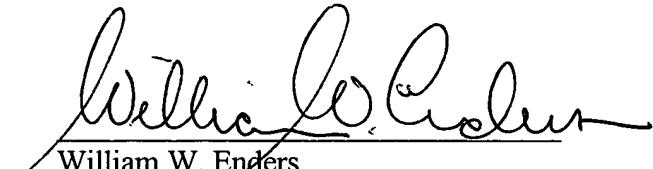
The present Supplemental Information and Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be

timely filed in accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with the filing of this Supplemental Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Commissioner is hereby authorized to deduct said fees from Deposit Account No. 10-1205/SURG:160.

A copy of the listed document(s) required by 37 C.F.R. § 1.98(a)(2) are enclosed for the convenience of the Examiner.

Applicant respectfully requests that the listed document(s) be made of record in the present case.

Respectfully submitted,



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Enclosures

Form PTO-1449 (modified)

List of Patents and Publications for Applicant's

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

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3753Other Art
See Pages 1-3**U.S. Patent Documents**

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Tech. Center 2100**Foreign Patent Documents**

| Exam. Init. | Ref. Des. | Document Number | Date | Country | Class | Sub Class | Translation Yes/No |
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| | C56 | Paxson, et al., "Wide-Area Traffic: The Failure of Poisson Modeling," 18 pages (July 18, 1995) |
| | C57 | Arlitt, et al., "Web Server Workload Characterization: The Search for Invariants (Extended Version)," 15 pages (March 15, 1996) |
| | C58 | Dilley, Web Server Workload Characterization," Hewlett-Packard Laboratories, 16 pages |
| | C59 | Mah, "An Empirical Model of HTTP Network Traffic," IEEE, 9 pages (April 1997) |
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| | C61 | Banga, et al., "Measuring the Capacity of a Web Server," 12 pages (December 1997) |
| | C62 | Arlitt, et al, "Workload Characterization of the 1998 World Cup Web Site," Hewlett-Packard Company, 81 pages (1999) |
| | C63 | Pitkow, "Web Characterization Activity Characterization Metrics," 4 pages (December 1998) |
| | C64 | Shenoy, et al, Symphony: An Integrated Multimedia File System," SPIE Vol. 3310, pp. 124-138 (1997) |
| | C65 | Choi, et al., "An Implementation Study of a Detection-Based Adaptive Block Replacement Scheme," 14 pages |
| | C66 | Chen, et al., "Effective Hardware-Based Data Prefetching for High-Performance Processors," IEEE Transactions on Computers 0018-9340/95 Vol. 44, No. 5, pp. 609-623 (May 1995) |
| | C67 | Karedla, et al., "Caching Strategies to Improve Disk System Performance," IEEE pp. 38-46 (February 1994) |

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| | C70 | Jouppi, Norman, "Improving Direct-Mapped Cache Performance by the Addition of a Small Fully-Associative Cache and Prefetch Buffers," IEEE, CH2887, pp. 364-373 (August 1990) |
| | C71 | Lee, et al., "LRFU (Least Recently/Frequently Used) Replacement Policy: A Spectrum of Block Replacement Policies," Technical Report SNU-CE-AN-96-004, Dept. of Computer Engineering, Seoul National Univ., 24 pages (July 1996) |
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| | C82 | O'Neil, et al., "The LRU-K Page Replacement Algorithm For Database Disk Buffering," pp. 297-306, ACM 0-89791-592 (May 1993) |

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| | C85 | Co-pending patent application serial no. 09/970,452 filed October 3, 2001, entitled Systems and Methods for Resource Monitoring In Information Storage Environments (SURG:159) |
| | C86 | Co-pending patent application serial no. 09/947,869 filed September 6, 2001, entitled Systems and Methods For Resource Management In Information Storage Environments (SURG:156) |

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